

AMENDMENTS TO THE CLAIMS

1-8. (Cancelled)

9. (New) A simulation-verification apparatus, comprising:

a controller-simulator computer, having control logic loaded therewith and operable to:

execute the control logic on a Linux operating system; and

output a control command signal according to an operating status of the control logic; and

a plant model-simulator computer, having plant model logic loaded therewith, wherein the plant model logic is operable to:

receive the control command signal output from said controller-simulator computer;

simulate an action status of a plant upon the receipt of the control command signal output from said controller-simulator computer; and

output a run status signal which represents the action status; and

wherein said plant model-simulator computer is operable to execute the plant model logic on a Linux operating system.

10. (New) The simulation-verification apparatus according to claim 9, wherein the control logic is a program operable to exercise run control of a combined cycle power plant, and wherein the plant model logic is a program operable to simulate a running action of the combined cycle power plant.

11. (New) A simulation-verification apparatus, comprising:

a controller-simulator computer, having control logic and a computation cycle managing task loaded therewith,

wherein the control logic operates according to a computation cycle, in which the computation cycle must be set,

wherein the computation cycle managing task is operable to set the computation cycle of the control logic,
 wherein said controller-simulator is operable to:
 execute the control logic, according to the computation cycle set by the computation cycle managing task, on a Linux operating system; and
 output a control command signal according to an operating status of the control logic; and
 a plant model-simulator computer, having plant model logic and a computation cycle managing task loaded therewith,
 wherein the plant model logic is operates according to a computation cycle, in which the computation cycle must be set,
 wherein the plant model logic is operable to:
 receive the control command signal output from said controller-simulator computer;
 simulate an action status of a plant upon the receipt of the control command signal output from said controller-simulator computer; and
 output a run status signal which represents the action status,
 wherein the computation cycle managing task is operable to set the computation cycle of the plant model logic, and
 wherein said plant model-simulator computer is operable to execute the plant model logic, according to the computation cycle set by the computation cycle managing task, on a Linux operating system.

12. (New) The simulation-verification apparatus according to claim 11, wherein the control logic is a program operable to exercise run control of a combined cycle power plant, and the plant model logic is a program operable to simulate a running action of the combined cycle power plant.

13. (New) A simulation-verification apparatus, comprising:

- a controller-simulator computer, having control logic and a computation cycle managing task loaded therewith,
 - wherein the control logic operates according to a computation cycle or a computation status
 - wherein the computation cycle managing task operable to set the computation cycle of the control logic, and
 - wherein said controller-simulator computer is operable to:
 - store the computation status of the control logic in a storage part of said controller-simulator computer;
 - execute the control logic, according to either the computation cycle set by the computation cycle managing task or the computation status stored in the storage part of said controller-simulator device, on a Linux operating system; and
 - output a control command signal according to an operating status of the control logic; and
- a plant model-simulator computer, having plant model logic and a computation cycle managing task loaded therewith,
 - wherein the plant model logic operates according to a computation cycle or a computation status,
 - wherein the plant model logic is operable to:
 - receive the control command signal output from said controller-simulator computer;
 - simulate an action status of a plant upon the receipt of the control command signal output from said controller-simulator computer; and
 - output a run status signal which represents the action status,
 - wherein the computation cycle managing task operable to set the computation cycle of the plant model logic, and
 - wherein said plant model-simulator is operable to:

store the computation status of the plant model logic in a storage part of said plant model-simulator computer; and

execute said plant model logic, according to either the computation cycle set by the computation cycle managing task or the computation status stored in the storage part of said plant model-simulator computer, on a Linux operating system.

14. (New) The simulation-verification apparatus according to claim 13, wherein the control logic is a program operable to exercise run control of a combined cycle power plant, and the plant model logic is a program operable to simulate a running action of the combined cycle power plant.